



SHEET 1 OF 1

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 245646US2		SERIAL NO. 10/717,595	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT Joichiro EZAKI, et al.			
				FILING DATE November 21, 2003		GROUP <del>2655</del> 2827	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
MS	AA	6,269,027	07/31/2001	A. T. HURST, Jr., et al.			
	AB	5,894,447	04/13/1999	D. TAKASHIMA			
MS	AC	5,732,016	03/24/1998	E. CHEN, et al.			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
MS	AO	WO 2004/049344	06/10/2004	WIPO			
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
MS	AW	R. SCHEUERLEIN, et al., "A 10ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in each Cell", IEEE International Solid-State Circuits Conference, Session 7, Paper 7.2, 2000, pages 128-129					
	AX						
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner M. Tran					Date Considered 4/20/05		
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

PTO FORM 1449  
S/N 10/717595  
ART UNIT 2827



**LIST OF RELATED CASES**

	<u>Docket Number</u>	<u>Serial or Patent Number</u>	<u>Filing or Issue Date</u>	<u>Inventor/ Applicant</u>
MT	243211US2	10/669,561	09/25/03	EZAKI et al.
MT	245646US2*	10/717,595	11/21/03	EZAKI et al.

EXAMINER :

M. TRAN

Date 4/20/05